## Data sheet

SIMATIC S7-300, CPU 312C COMPACT CPU WITH MPI, 10 DI/6 DO, 2 FAST COUNTERS (10 KHZ), INTEGRATED 24V DC POWER SUPPLY, 64 KBYTE WORKING MEMORY, FRONT CONNECTOR (1 X 40PIN) AND MICRO MEMORY CARD REQUIRED



General information	
Hardware product version	01
Firmware version	V3.3
Engineering with	
Programming package	STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Mains buffering	
Mains/voltage failure stored energy time	5 ms
• Repeat rate, min.	1 s
Load voltage L+	
Digital outputs	
— Rated value (DC)	24 V

<ul> <li>Reverse polarity protection</li> </ul>	No
Input current	
Current consumption (rated value)	570 mA
Current consumption (in no-load operation), typ.	90 mA
Inrush current, typ.	5 A
l²t	0.7 A <sup>2</sup> ·s
Digital outputs	
• from load voltage L+, max.	25 mA
Power loss	
Power loss, typ.	8 W
Memory	
Work memory	
• integrated	64 kbyte
• expandable	No
Size of retentive memory for retentive data	64 kbyte
blocks	
Load memory	
• Plug-in (MMC)	Yes
<ul><li>Plug-in (MMC), max.</li></ul>	8 Mbyte
<ul> <li>Data management on MMC (after last</li> </ul>	10 y
programming), min.	
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
• without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.1 µs
for word operations, typ.	0.24 μs
for fixed point arithmetic, typ.	0.32 μs
for floating point arithmetic, typ.	1.1 µs
CPU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	
Number, max.	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte
FB	
• Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
• Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte

OB	
Description	see instruction list
• Size, max.	64 kbyte
<ul> <li>Number of free cycle OBs</li> </ul>	1; OB 1
<ul> <li>Number of time alarm OBs</li> </ul>	1; OB 10
<ul> <li>Number of delay alarm OBs</li> </ul>	2; OB 20, 21
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	4; OB 32, 33, 34, 35
<ul> <li>Number of process alarm OBs</li> </ul>	1; OB 40
<ul> <li>Number of startup OBs</li> </ul>	1; OB 100
<ul> <li>Number of asynchronous error OBs</li> </ul>	4; OB 80, 82, 85, 87
<ul> <li>Number of synchronous error OBs</li> </ul>	2; OB 121, 122
Nesting depth	
• per priority class	16
additional within an error OB	4
Counters, timers and their retentivity	

Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	
• Number	Unlimited (limited only by RAM capacity)
S7 times	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	No retentivity
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)

Feliang	Data areas and their retentivity	
Number, max.     Retentivity available     Retentivity preset     Number of clock memories     Number of clock memories     Number of clock memories     Number, max.     Number, max.     Number, max.     Number, max.     Number of clock memories     Number, max.     Number of clock memories     Number, max.     Number, max.     Number of clock memories     Number, max.     Number, max.     Number of clock memories     Number of clock memories     Number, max.     Number of clock memories     Numbe		All, max. 64 KB
Retentivity available     Retentivity preset     Retentivity preset     Number of clock memories     Number of clock memories     Number, max.     Number, max.     Retentivity adjustable     Retentivity adjustable     Retentivity preset     Retentivity adjustable     Retentivity preset     Retentivity adjustable     Retentivity adjus	Flag	
Retentivity preset     Number of clock memories     Rumber, max.	Number, max.	256 byte
Number of clock memories  Pata blocks  Number, max.  Number, max.  Size, max.  Retentivity adjustable Retentivity preset  Retentivity preset  Per priority class, max.  1 024: Number range: 1 to 16000  Retentivity preset  Yes; via non-retain property on DB  Retentivity preset  Per priority class, max.  32 kbyte; Max. 2048 bytes per block  Address area  I/O address area	Retentivity available	Yes; MB 0 to MB 255
Number, max.	<ul> <li>Retentivity preset</li> </ul>	MB 0 to MB 15
Number, max.     Size, max.     Size, max.     Retentivity adjustable     Retentivity preset     Yes, via non-retain property on DB     Retentivity preset     Yes     Retentivity preset     Yes     Vacal data     Per priority class, max.  Address area  I/O a	<ul> <li>Number of clock memories</li> </ul>	8; 1 memory byte
Size, max. Retentivity adjustable Retentivity preset  Ves  Local data  Per priority class, max.  32 kbyte; Max. 2048 bytes per block  Address area  I/O address area  I/O address area  I/O uputs Outputs Outputs Outputs  I 024 byte Outputs  I 024 byte  I 024 byte  I 024 byte  I 024 byte  Outputs Outputs Outputs Outputs I 1 024 byte  I 024 byte  Outputs Outputs I 1 024 byte  I 1	Data blocks	
Retentivity adjustable Retentivity preset  Retentivity preset  Yes  Local data  per priority class, max.  32 kbyte; Max. 2048 bytes per block  Address area  liputs liputs, adjustable liputs, adjustable liputs, default liputs li	<ul><li>Number, max.</li></ul>	1 024; Number range: 1 to 16000
Retentivity preset     Local data	● Size, max.	64 kbyte
Local data  • per priority class, max.  32 kbyte; Max. 2048 bytes per block  Address area  // O address area  • Inputs • Outputs  1 024 byte  Outputs  of which distributed  — Inputs — Outputs  none  Process image  • Inputs • Outputs  1 024 byte  • Outputs  • Inputs • Outputs  • Inputs, adjustable • Inputs, adjustable • Inputs, default • Outputs, default  • Outputs, default  1 024 byte  Inputs, default  2 8 byte  Default addresses of the integrated channels  — Digital inputs — Digital outputs  1 24.0 to 125.1 — Digital channels  Inputs — of which central  2 66  Outputs — of which central  2 66  Inputs — of which central  2 62  Analog channels  Inputs — of which central  6 4  Outputs — of which central  6 4  Outputs — of which central  6 4	Retentivity adjustable	Yes; via non-retain property on DB
	<ul> <li>Retentivity preset</li> </ul>	Yes
Address area	Local data	
I/O address area     Inputs	• per priority class, max.	32 kbyte; Max. 2048 bytes per block
I/O address area     Inputs	Address area	
Outputs     of which distributed         — Inputs         — Outputs         — Outputs         — Outputs  Process image  Inputs         — Inputs         — Outputs  Outputs         — Outputs  Inputs         — Outputs  Inputs         — Outputs  Inputs  Inputs         — Outputs, adjustable  Inputs, adjustable  Inputs, default  Inputs, default  Outputs, default  Inputs, default  Inputs  Inputs         — Digital inputs  Inputs         — Digital outputs  Inputs         — of which central  Outputs  Inputs         — Owhich central  Inputs         — Of which central  Outputs         — Of which central  Inputs         — Of which central  Inputs         — Of which central  Outputs         — Of which central  Outputs  Outputs         — Of which central  Outputs         — Of which central  Outputs         — Of which central  Outputs         — Outputs         — Of which central  Outputs		
of which distributed  — Inputs — Outputs none  Process image  ● Inputs ● Outputs 1 024 byte ● Outputs, adjustable ● Outputs, adjustable ● Outputs, adjustable ● Inputs, default ● Outputs, default ● Outputs, default ■ Outputs, default ■ Default addresses of the integrated channels  — Digital inputs — Digital outputs  124.0 to 125.1 — Digital outputs  Digital channels  ● Inputs ● Outputs 266 ● Outputs — of which central 266 ● Outputs — of which central 262  Analog channels  ● Inputs ● Inputs — Of which central 262  Analog channels  ● Inputs — of which central 264 ● Outputs — of which central 265  Analog channels  ● Inputs — of which central 64 ● Outputs — of which central 64 ● Outputs — of which central 64 ● Outputs	• Inputs	1 024 byte
Inputs none Outputs none  Process image  ■ Inputs 1 024 byte ■ Outputs 1 024 byte ■ Outputs, adjustable 1 024 byte ■ Outputs, adjustable 1 024 byte ■ Outputs, default 128 byte ■ Inputs, default 128 byte ■ Outputs, default 128 byte  Default addresses of the integrated channels Digital inputs 124.0 to 125.1 Digital outputs 124.0 to 124.5  Digital channels  ■ Inputs 266 Of which central 266 ■ Outputs 262 of which central 262  Analog channels  ■ Inputs 64 Of which central 64 ■ Outputs 64	Outputs	1 024 byte
— Outputs none  Process image  Inputs 1 024 byte Outputs 1 024 byte Inputs, adjustable 1 024 byte Outputs, adjustable 1 024 byte Outputs, default 128 byte Inputs, default 128 byte Outputs, default 128 byte Default addresses of the integrated channels — Digital inputs 124.0 to 125.1 — Digital outputs 124.0 to 124.5  Digital channels  Inputs 266 — of which central 266 Outputs 262 — of which central 262  Analog channels  Inputs 64 — of which central 64 Outputs 64 Outputs 64	of which distributed	
Process image	— Inputs	none
<ul> <li>Inputs</li> <li>Outputs</li> <li>1 024 byte</li> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>1 024 byte</li> <li>Outputs, adjustable</li> <li>Inputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>124.0 to 125.1</li> <li>— Digital channels</li> <li>Inputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Of which central</li> <li>Inputs</li> <li>On which central</li> <li>Outputs</li> </ul>	— Outputs	none
<ul> <li>Outputs</li> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>Outputs, default</li> <li>Inputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>Digital channels</li> <li>Inputs</li> <li>— of which central</li> <li>Outputs</li> <li>Outputs</li> <li>Inputs</li> <li>Outputs</li> </ul>	Process image	
<ul> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>Inputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>Digital channels</li> <li>Inputs</li> <li>— of which central</li> <li>Outputs</li> <li>Outputs</li> <li>Inputs</li> <li>— of which central</li> <li>Poutputs</li> <li>— of which central</li> <li>Inputs</li> <li>— of which central</li> <li>Outputs</li> <li>— of which central</li> <li>Outputs</li> <li>— of which central</li> <li>Outputs</li> <li>Outputs</li> <li>— of which central</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> </ul>	• Inputs	1 024 byte
<ul> <li>Outputs, adjustable</li> <li>Inputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>124.0 to 125.1</li> <li>— Digital channels</li> <li>Inputs</li> <li>Of which central</li> <li>Outputs</li> <li>Outputs</li> <li>Inputs</li> <li>Of which central</li> <li>Outputs</li> <li>Of which central</li> <li>Inputs</li> <li>Of which central</li> <li>Outputs</li> </ul>	Outputs	1 024 byte
<ul> <li>Inputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>124.0 to 125.1</li> <li>— Digital channels</li> <li>Inputs</li> <li>— of which central</li> <li>Outputs</li> <li>— of which central</li> <li>Possible of the integrated channels</li> <li>124.0 to 125.1</li> <li>124.0 to 124.5</li> <li>Digital channels</li> <li>Inputs</li> <li>— of which central</li> <li>■ Outputs</li> <li>— of which central</li> <li>Inputs</li> <li>— of which central</li> <li>— of which central</li> <li>● Outputs</li> <li>● Outputs</li> <li>● Outputs</li> <li>● Outputs</li> <li>● Outputs</li> <li>● Outputs</li> </ul>	<ul><li>Inputs, adjustable</li></ul>	1 024 byte
Outputs, default     Default addresses of the integrated channels     — Digital inputs     — Digital outputs     124.0 to 125.1     — Digital outputs      124.0 to 124.5  Digital channels      • Inputs     — of which central     • Outputs     — of which central     262     — of which central     262  Analog channels      • Inputs     — of which central     64     — of which central     • Outputs     — of which central     64     — of which central     • Outputs     — of which central     64     — of which central     • Outputs     • Outputs     64     — of which central     • Outputs	<ul> <li>Outputs, adjustable</li> </ul>	1 024 byte
Default addresses of the integrated channels         — Digital inputs       124.0 to 125.1         — Digital outputs       124.0 to 124.5         Digital channels         • Inputs       266         — of which central       262         — of which central       262         Analog channels       64         • Inputs       64         — of which central       64         • Outputs       64         • Outputs       64	• Inputs, default	128 byte
— Digital inputs       124.0 to 125.1         — Digital outputs       124.0 to 124.5         Digital channels         ● Inputs       266         — of which central       262         — of which central       262         Analog channels         ● Inputs       64         — of which central       64         ● Outputs       64         ● Outputs       64	Outputs, default	128 byte
— Digital outputs       124.0 to 124.5         Digital channels       266         ● Inputs       266         — of which central       262         — of which central       262         Analog channels       64         — of which central       64         ● Outputs       64         ● Outputs       64	Default addresses of the integrated channels	
Digital channels       266         ● Inputs       266         — of which central       262         — of which central       262         Analog channels       64         — of which central       64         ● Outputs       64         ● Outputs       64	— Digital inputs	124.0 to 125.1
● Inputs       266         — of which central       262         ● Outputs       262         — of which central       262         Analog channels       64         — of which central       64         • Outputs       64	— Digital outputs	124.0 to 124.5
— of which central       266         ● Outputs       262         — of which central       262         Analog channels         ● Inputs       64         — of which central       64         ● Outputs       64	Digital channels	
● Outputs       262         — of which central       262         Analog channels         ● Inputs       64         — of which central       64         ● Outputs       64	• Inputs	266
— of which central 262  Analog channels  ● Inputs 64  — of which central 64  • Outputs 64	— of which central	266
Analog channels  Inputs  of which central  Outputs  64  64  64	Outputs	262
<ul> <li>Inputs</li> <li>of which central</li> <li>Outputs</li> <li>64</li> <li>64</li> <li>64</li> </ul>	— of which central	262
of which central 64  Outputs 64	Analog channels	
• Outputs 64	• Inputs	64
	— of which central	64
— of which central 64	Outputs	64
	— of which central	64

Hardware configuration	
Number of expansion units, max.	0
Number of DP masters	
• integrated	none
• via CP	4
Number of operable FMs and CPs (recommended)	
● FM	8
• CP, PtP	8
• CP, LAN	4
Rack	
● Racks, max.	1
<ul><li>Modules per rack, max.</li></ul>	8
Time of day	
Clock	
Software clock	Yes
<ul> <li>retentive and synchronizable</li> </ul>	No; Buffered: No, Can be synchronized: Yes
<ul> <li>Deviation per day, max.</li> </ul>	10 s; Typ.: 2 s
<ul> <li>Behavior of the clock following POWER-ON</li> </ul>	The clock continues at the time of day it had when power was switched off
Operating hours counter	
• Number	1
<ul><li>Number/Number range</li></ul>	0
<ul><li>Range of values</li></ul>	0 to 2^31 hours (when using SFC 101)
Granularity	1 hour
• retentive	Yes; Must be restarted at each restart
Clock synchronization	
• supported	Yes
● to MPI, master	Yes
• to MPI, slave	Yes
• in AS, master	Yes
• in AS, slave	No
Digital inputs	
Number of digital inputs	10
<ul> <li>of which inputs usable for technological functions</li> </ul>	8
integrated channels (DI)	10
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	10

— up to 60 °C, max.	5
vertical installation	
— up to 40 °C, max.	5
Input voltage	
Rated value (DC)	24 V
·	-3 to +5V
• for signal "0"	+15 to +30V
• for signal "1"	+15 to +300
Input current	0.44
• for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	V 04/00/0/45 AV 5 H : 111 5
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for counter/technological functions	
— at "0" to "1", max.	48 μs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
• shielded, max.	1 000 m; 100 m for technological functions
• unshielded, max.	600 m; For technological functions: No
for technological functions	
— shielded, max.	100 m; at maximum count frequency
— unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	6
of which high-speed outputs	2; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	6
Short-circuit protection	Yes; Clocked electronically
<ul> <li>Response threshold, typ.</li> </ul>	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digital input	Yes
Switching capacity of the outputs	
• on lamp load, max.	5 W
Load resistance range	
• lower limit	48 Ω
• upper limit	4 kΩ
Output voltage	
• for signal "1", min.	L+ (-0.8 V)
Output current	
• for signal "1" rated value	500 mA

• for signal "1" permissible range, min.	5 mA
• for signal "1" permissible range, max.	0.6 A
• for signal "1" minimum load current	5 mA
• for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	
• for uprating	No
• for redundant control of a load	Yes
Switching frequency	
with resistive load, max.	100 Hz
<ul><li>with inductive load, max.</li></ul>	0.5 Hz
• on lamp load, max.	100 Hz
• of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	2 A
— up to 60 °C, max.	1.5 A
vertical installation	
— up to 40 °C, max.	1.5 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	0
integrated channels (AI)	0
Analagastasta	
Analog outputs  Number of analog outputs	0
integrated channels (AO)	0
integrated charmers (70)	ŭ
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
permissible quiescent current (2-wire	1.5 mA
sensor), max.	
Interfaces	
Number of industrial Ethernet interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	0
1. Interface	
Interface type	Integrated RS 485 interface
Physics	RS 485
Isolated	No

Power supply to interface (15 to 30 V DC), max.	200 mA
Functionality	
• MPI	Yes
PROFIBUS DP master	No
PROFIBUS DP slave	No
Point-to-point connection	No
MPI	
Transmission rate, max.	187.5 kbit/s
Services	
— PG/OP communication	Yes
— Routing	No
Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes; Only server, configured on one side
— S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes
01 0011111a1110a11011, ac 001101	
Communication functions	
PG/OP communication	Yes
Data record routing	No
Global data communication	V
• supported	Yes
<ul> <li>Number of GD loops, max.</li> </ul>	8
<ul> <li>Number of GD packets, max.</li> </ul>	8
<ul> <li>Number of GD packets, transmitter, max.</li> </ul>	8
<ul> <li>Number of GD packets, receiver, max.</li> </ul>	8
<ul> <li>Size of GD packets, max.</li> </ul>	22 byte
<ul> <li>Size of GD packet (of which consistent), max.</li> </ul>	22 byte
S7 basic communication	
• supported	Yes
<ul> <li>User data per job, max.</li> </ul>	76 byte
<ul> <li>User data per job (of which consistent), max.</li> </ul>	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
<ul> <li>User data per job, max.</li> </ul>	180 byte; (with PUT/GET)
• User data per job (of which consistent), max.	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	

• overall	6
<ul> <li>usable for PG communication</li> </ul>	5
<ul> <li>reserved for PG communication</li> </ul>	1
— adjustable for PG communication, min.	1
— adjustable for PG communication, max.	5
<ul> <li>usable for OP communication</li> </ul>	5
<ul> <li>reserved for OP communication</li> </ul>	1
— adjustable for OP communication, min.	1
— adjustable for OP communication, max.	5
<ul> <li>usable for S7 basic communication</li> </ul>	2
— reserved for S7 basic communication	0
<ul> <li>adjustable for S7 basic communication, min.</li> </ul>	0
<ul><li>— adjustable for S7 basic communication, max.</li></ul>	2

S7 message functions	
Number of login stations for message functions, max.	6; Depending on the configured connections for PG/OP and S7
	basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300
Test commissioning functions	
Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4

Status block	res, up to 2 simultaneously
Single step	Yes
Number of breakpoints	4
Status/control	
Status/control variable	Yes
<ul><li>Variables</li></ul>	Inputs, outputs, memory bits, DB, times, counters
<ul> <li>Number of variables, max.</li> </ul>	30
— of which status variables, max.	30
— of which control variables, max.	14
Forcing	
• Forcing	Yes

Ctatas/control variable	
<ul> <li>Variables</li> </ul>	Inputs, outputs, memory bits, DB, times, counters
<ul> <li>Number of variables, max.</li> </ul>	30
— of which status variables, max.	30
— of which control variables, max.	14
Forcing	
• Forcing	Yes
<ul> <li>Forcing, variables</li> </ul>	Inputs, outputs
<ul><li>Number of variables, max.</li></ul>	10
Diagnostic buffer	
• present	Yes
<ul> <li>Number of entries, max.</li> </ul>	500
— adjustable	No
<ul><li>of which powerfail-proof</li></ul>	100; Only the last 100 entries are retained
<ul> <li>Number of entries readable in RUN, max.</li> </ul>	499
— can be set	Yes; From 10 to 499

— preset	10	
Service data		
• can be read out	Yes	
Interrupts/diagnostics/status information		
Diagnostics indication LED		
Status indicator digital input (green)	Yes	
<ul> <li>Status indicator digital output (green)</li> </ul>	Yes	
Integrated Functions		
Number of counters	2; See "Technological Functions" manual	
Counting frequency (counter) max.	10 kHz	
Frequency measurement	Yes	
Number of frequency meters	2; up to 10 kHz (see "Technological Functions" manual)	
controlled positioning	No	
integrated function blocks (closed-loop control)	No	
PID controller	No	
Number of pulse outputs	2; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)	
Limit frequency (pulse)	2.5 kHz	
Potential separation		
Potential separation digital inputs		
Potential separation digital inputs	Yes	
• between the channels	No	
between the channels and backplane bus	Yes	
Potential separation digital outputs		
Potential separation digital outputs	Yes	
between the channels	No	
• between the channels and backplane bus	Yes	
Isolation		
Isolation tested with	600 V DC	
Ambient conditions		
Ambient temperature during operation		
• min.	0 °C	
• max.	60 °C	
Configuration		
Configuration software		
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203	
• STEP 7 Lite	No	
Programming		
Command set	see instruction list	

<ul> <li>Nesting levels</li> </ul>	8
<ul><li>System functions (SFC)</li></ul>	see instruction list
<ul> <li>System function blocks (SFB)</li> </ul>	see instruction list
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— GRAPH	Yes
— HiGraph®	Yes
Know-how protection	
User program protection/password protection	Yes
<ul> <li>Block encryption</li> </ul>	Yes; With S7 block Privacy
Dimensions	
Width	80 mm
Height	125 mm
Depth	130 mm
Weights	
Weight, approx.	410 g
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